

FIG. 1

FIG. 2A	FIG. 2B	FIG. 2C
FIG. 2D	FIG. 2E	FIG. 2F
FIG. 2G		

FIG. 2

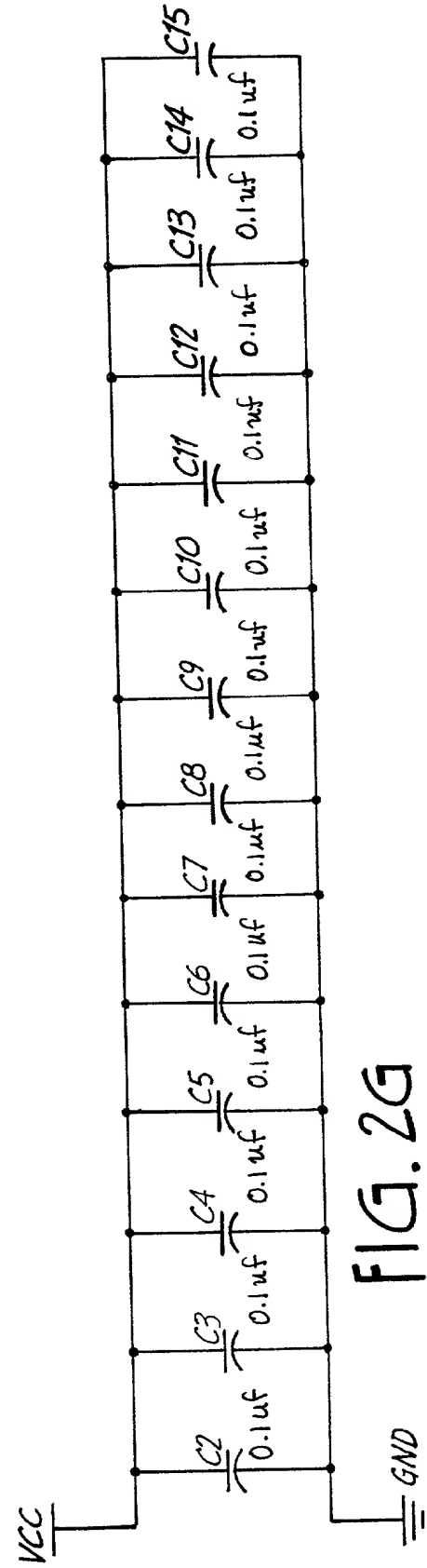


FIG. 2A is a schematic diagram of a microcontroller circuit. The circuit includes a microcontroller (U6) connected to a crystal oscillator (X1) and a 5V power supply (JP1). The microcontroller (U6) is a CY7C64011. The circuit also includes a 6MHz crystal oscillator (X1) and a 5V power supply (JP1). The microcontroller (U6) is connected to a 5V power supply (JP1) and a 6MHz crystal oscillator (X1). The circuit also includes a 6MHz crystal oscillator (X1) and a 5V power supply (JP1).

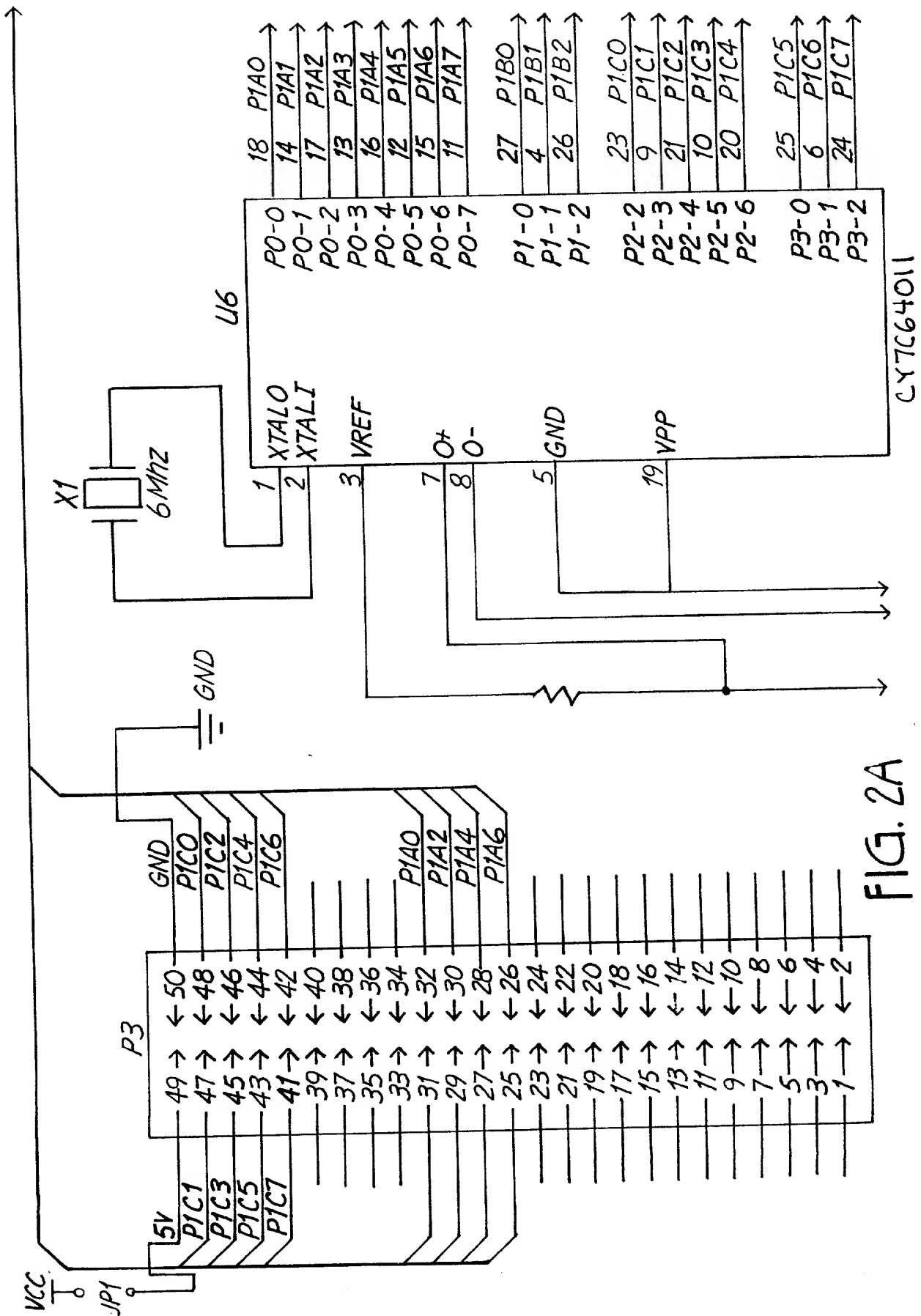


FIG. 2A

FIG. 2B is a schematic diagram of a circuit for driving a 16-bit parallel data bus. The circuit includes a 74HC573N (U2) and a 74HC573N (U4) connected to a 16-bit parallel data bus. The 74HC573N (U2) is connected to the 16-bit parallel data bus via its 16 data inputs (P1A0-P1A7, P1A8-P1A15). The 74HC573N (U4) is connected to the 16-bit parallel data bus via its 16 data outputs (P1A0-P1A7, P1A8-P1A15). The 74HC573N (U2) is also connected to a 16-bit parallel data bus via its 16 data outputs (P1A0-P1A7, P1A8-P1A15). The 74HC573N (U4) is also connected to a 16-bit parallel data bus via its 16 data outputs (P1A0-P1A7, P1A8-P1A15). The 74HC573N (U2) is also connected to a 16-bit parallel data bus via its 16 data outputs (P1A0-P1A7, P1A8-P1A15). The 74HC573N (U4) is also connected to a 16-bit parallel data bus via its 16 data outputs (P1A0-P1A7, P1A8-P1A15).

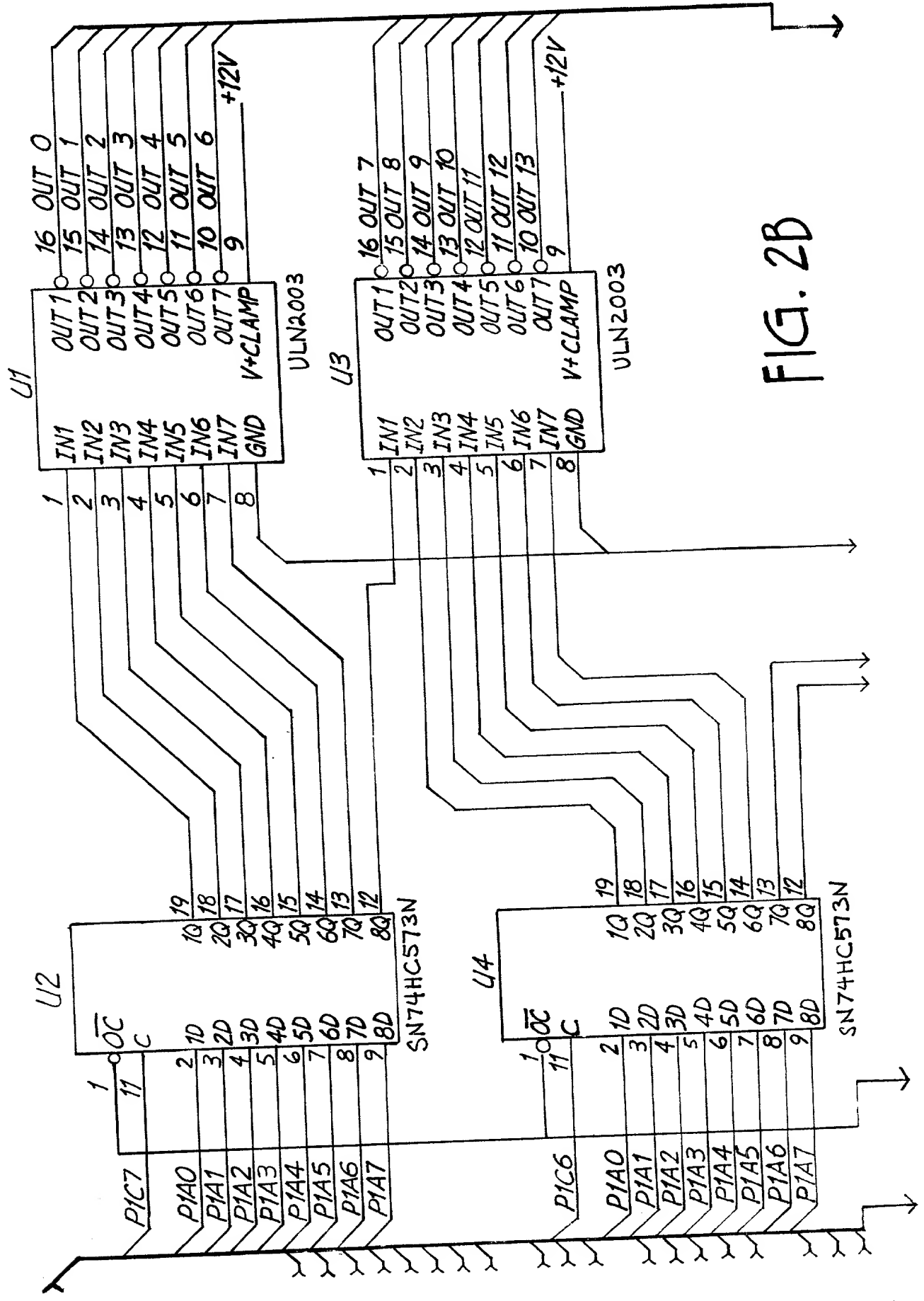


FIG. 2B

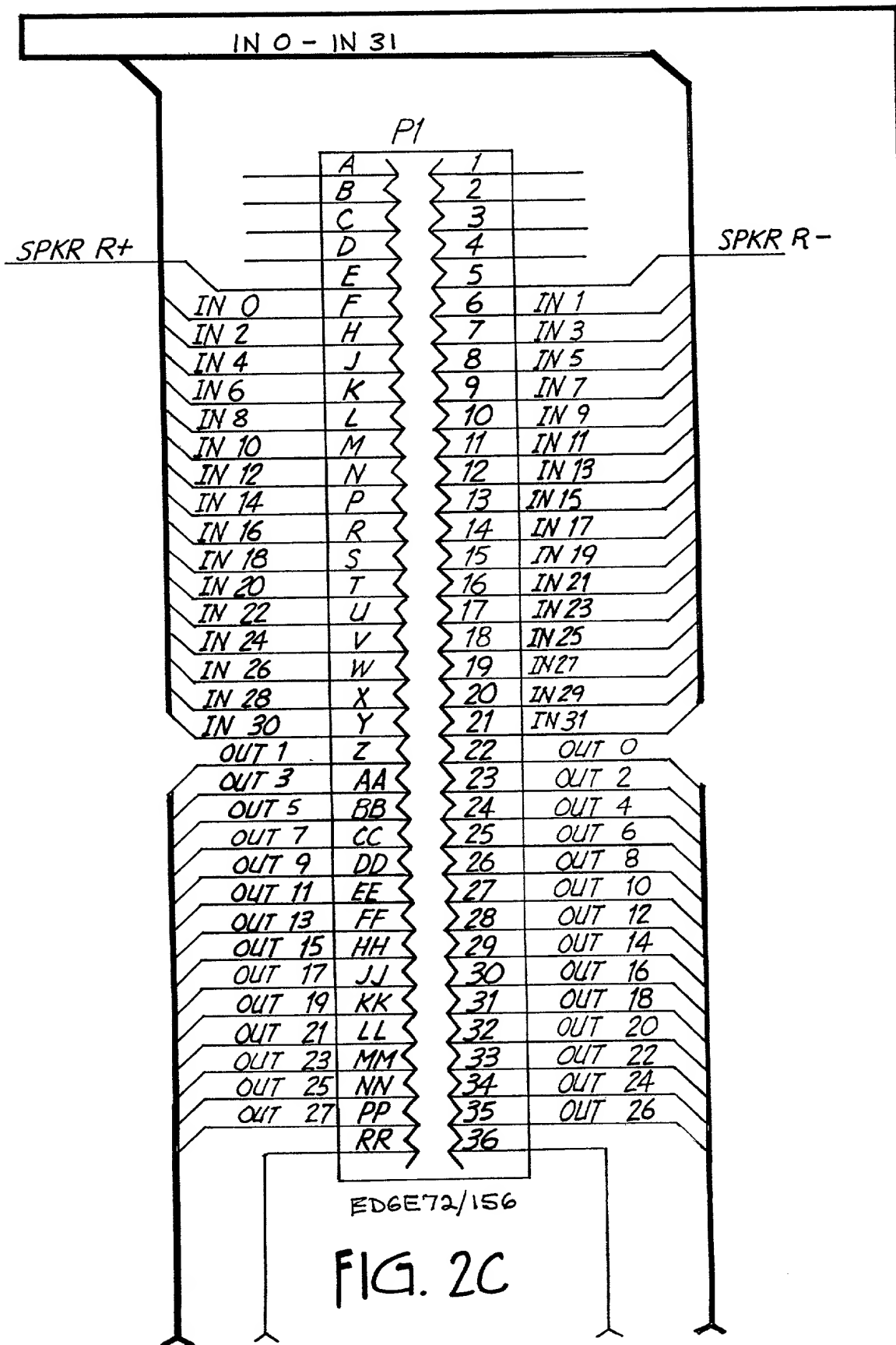


FIG. 2D

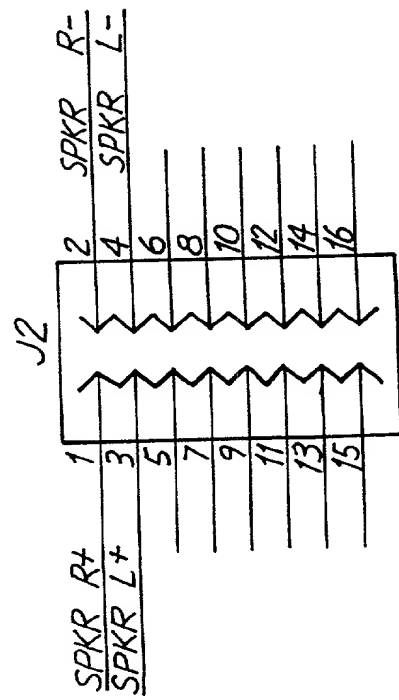
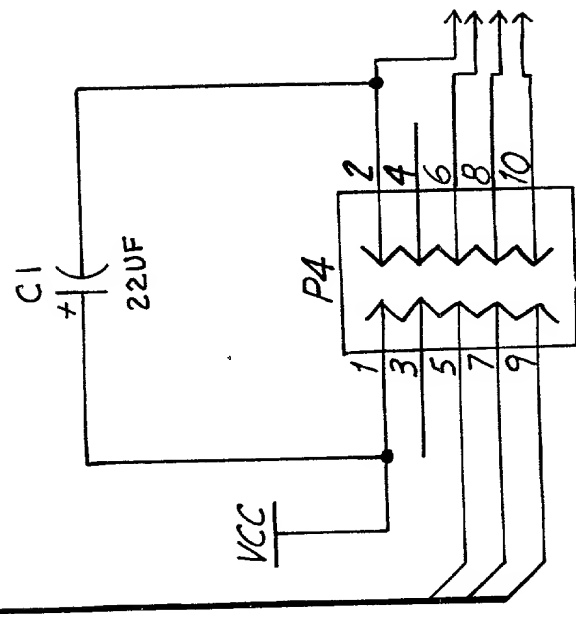
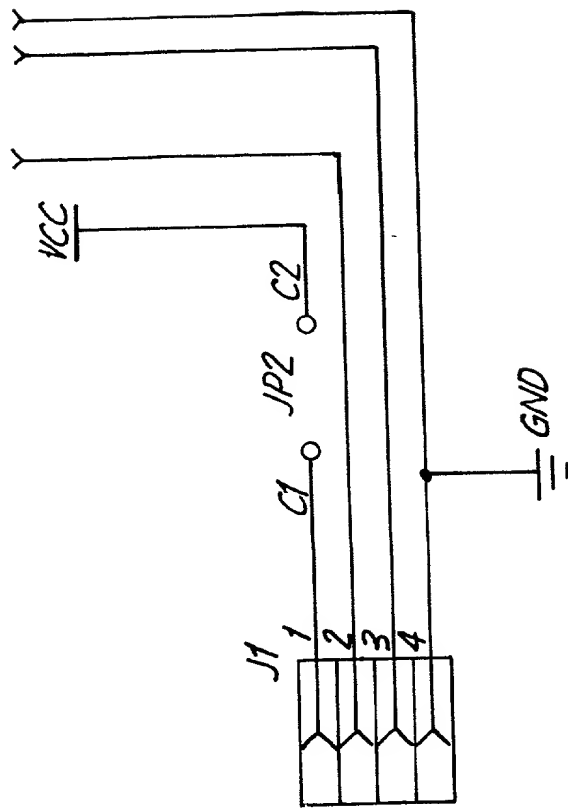


FIG. 2D

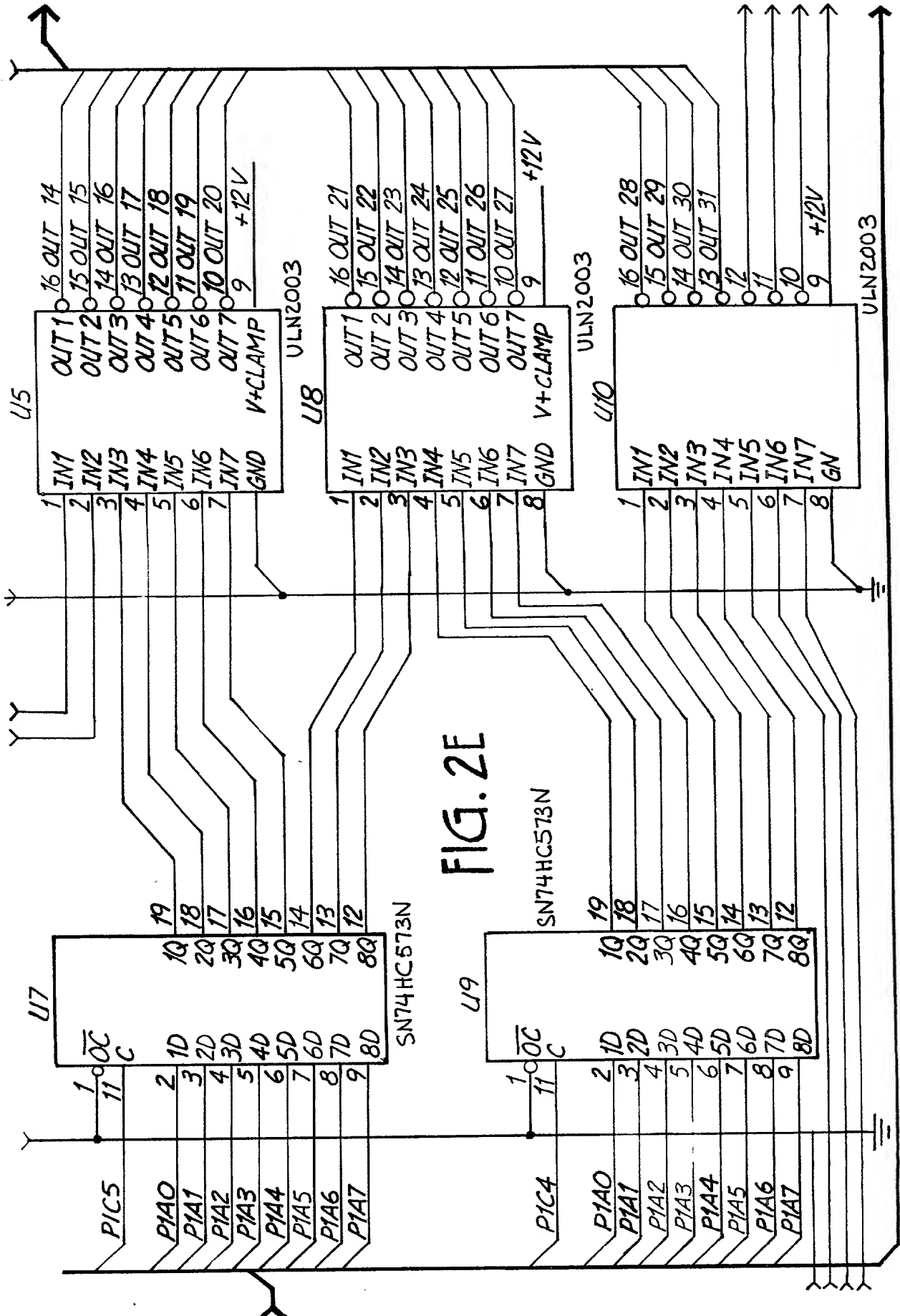
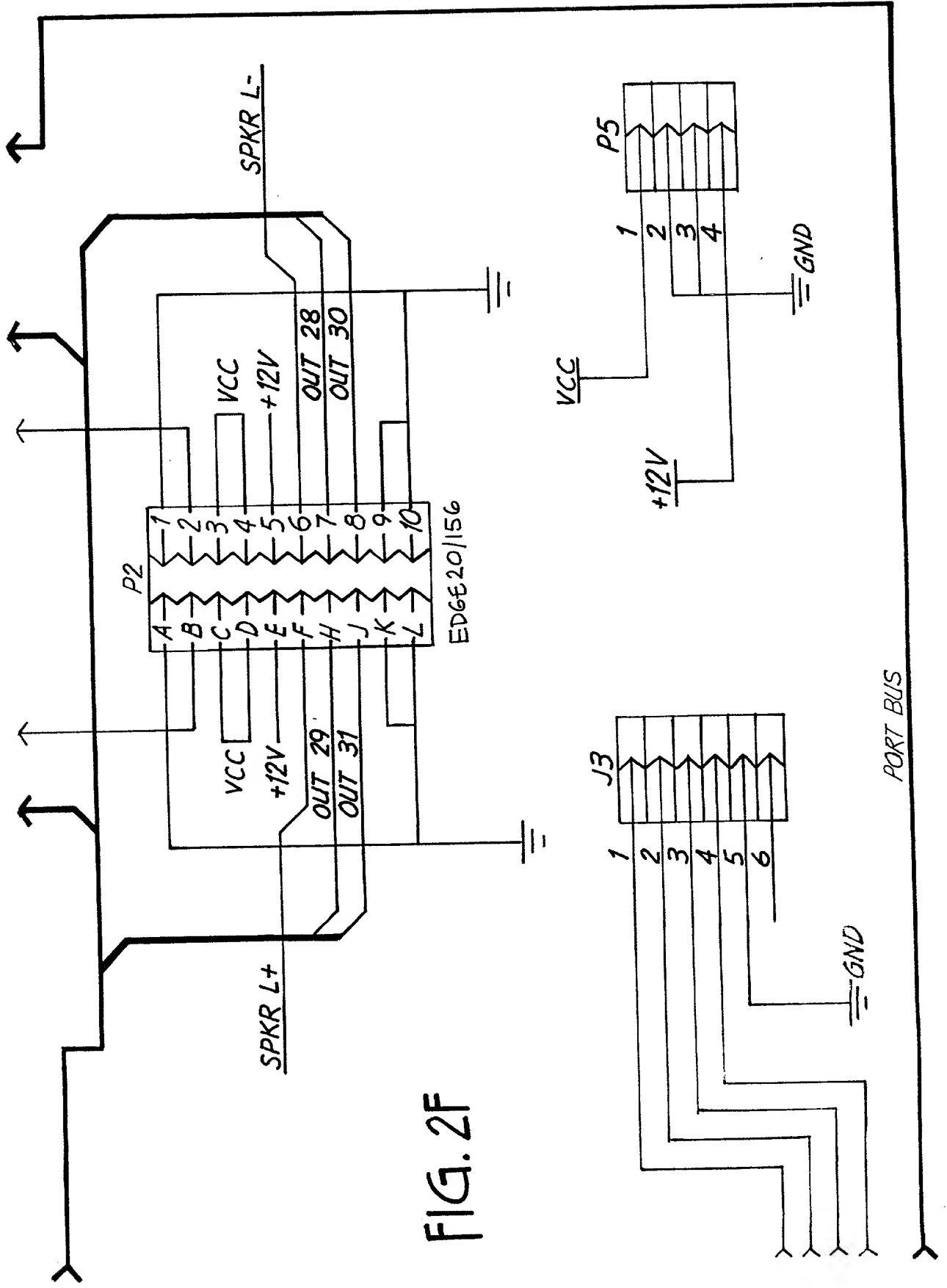


FIG. 2E

FIG. 2F



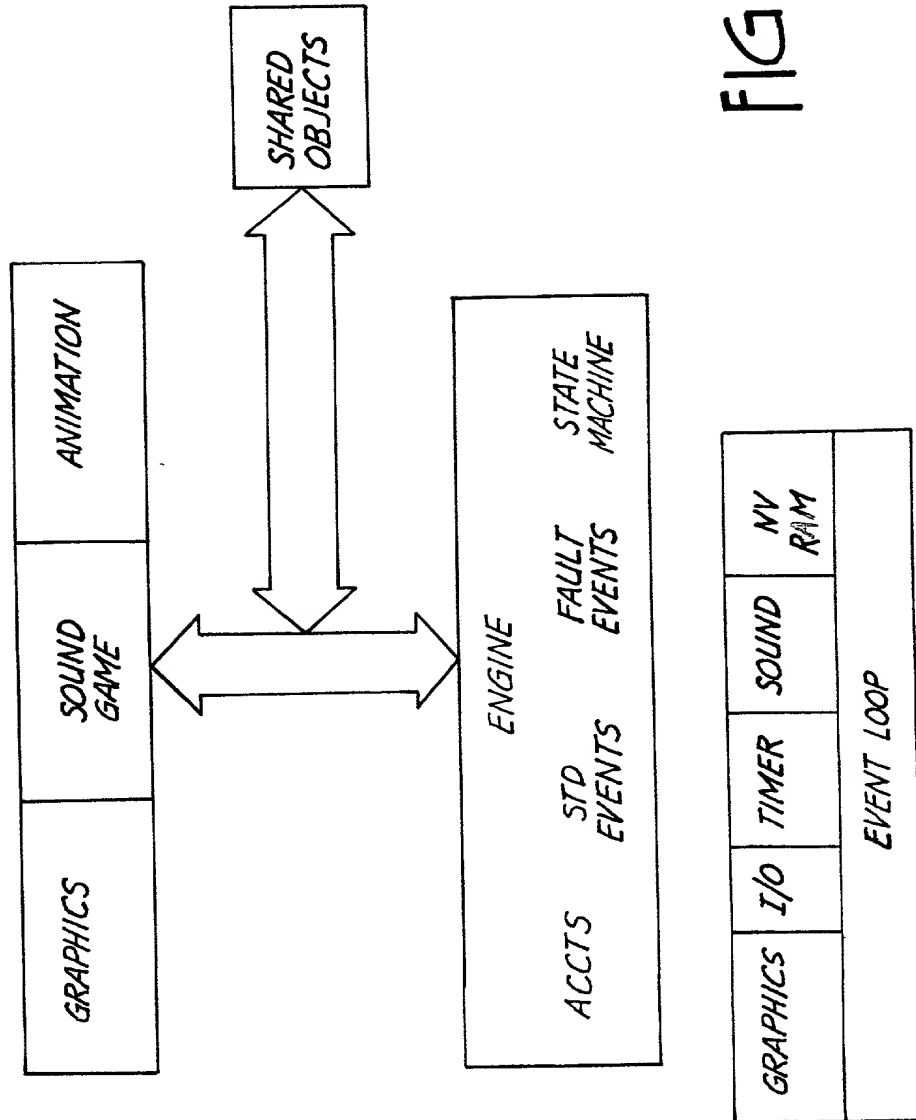


FIG. 3

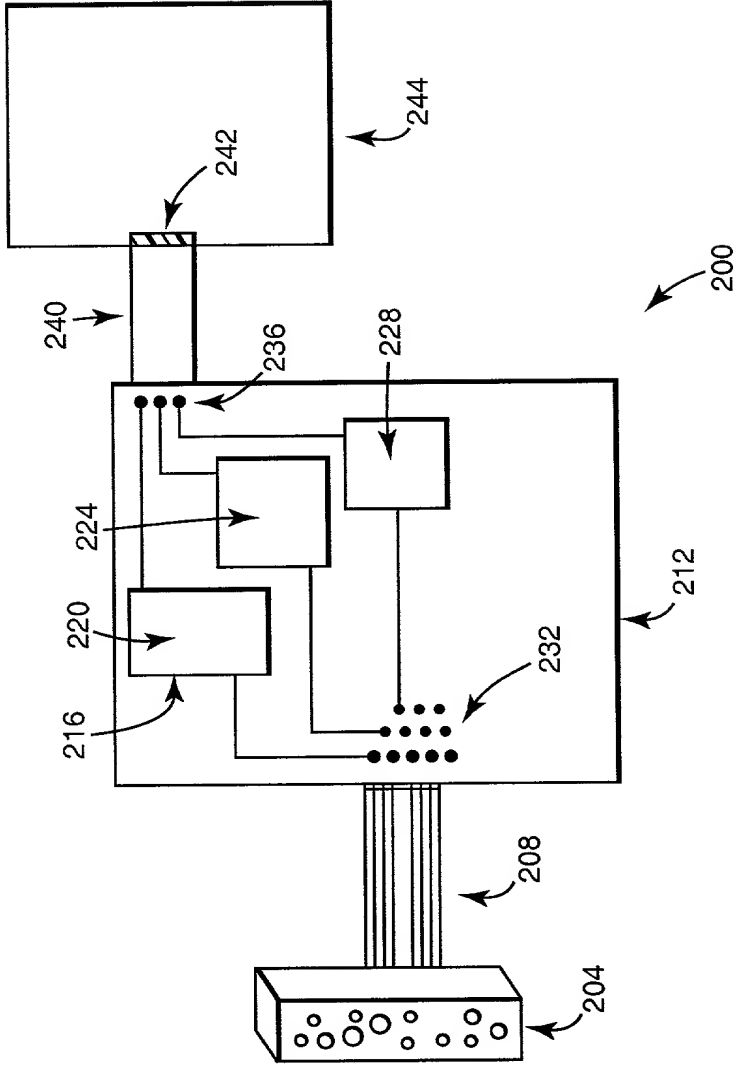


Fig. 4

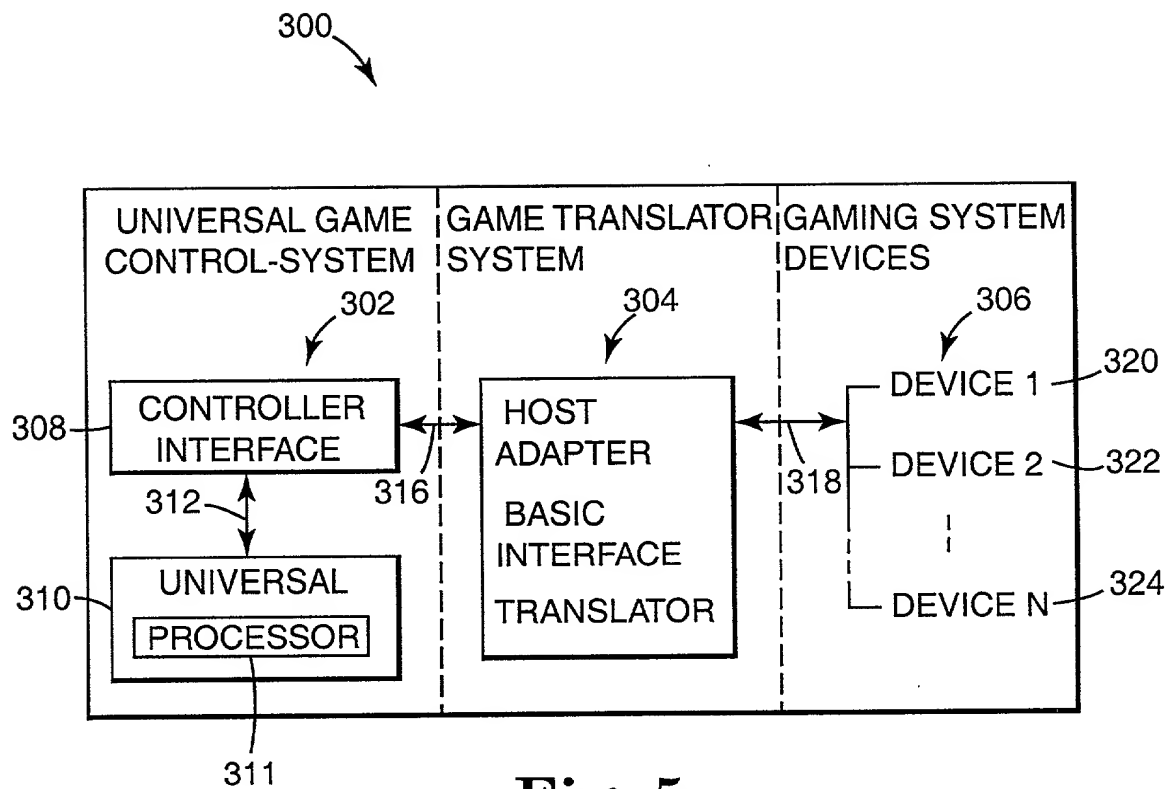


Fig. 5

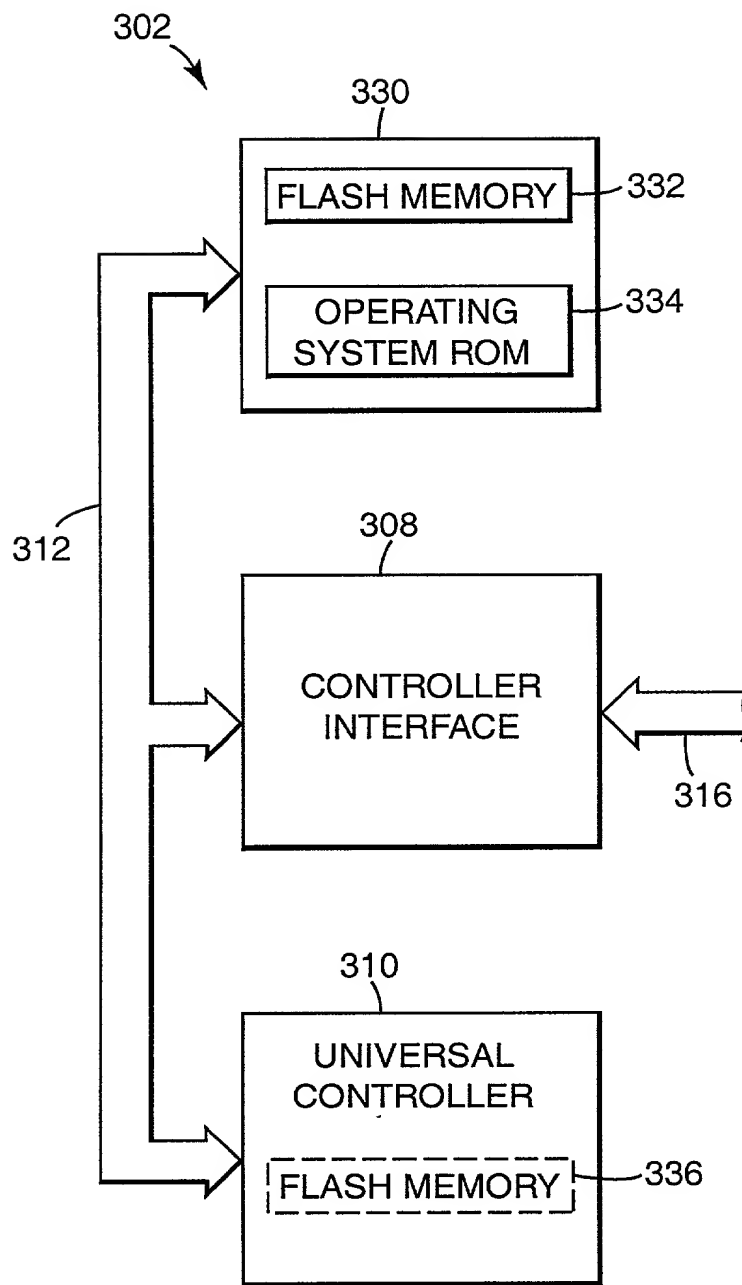


Fig. 6

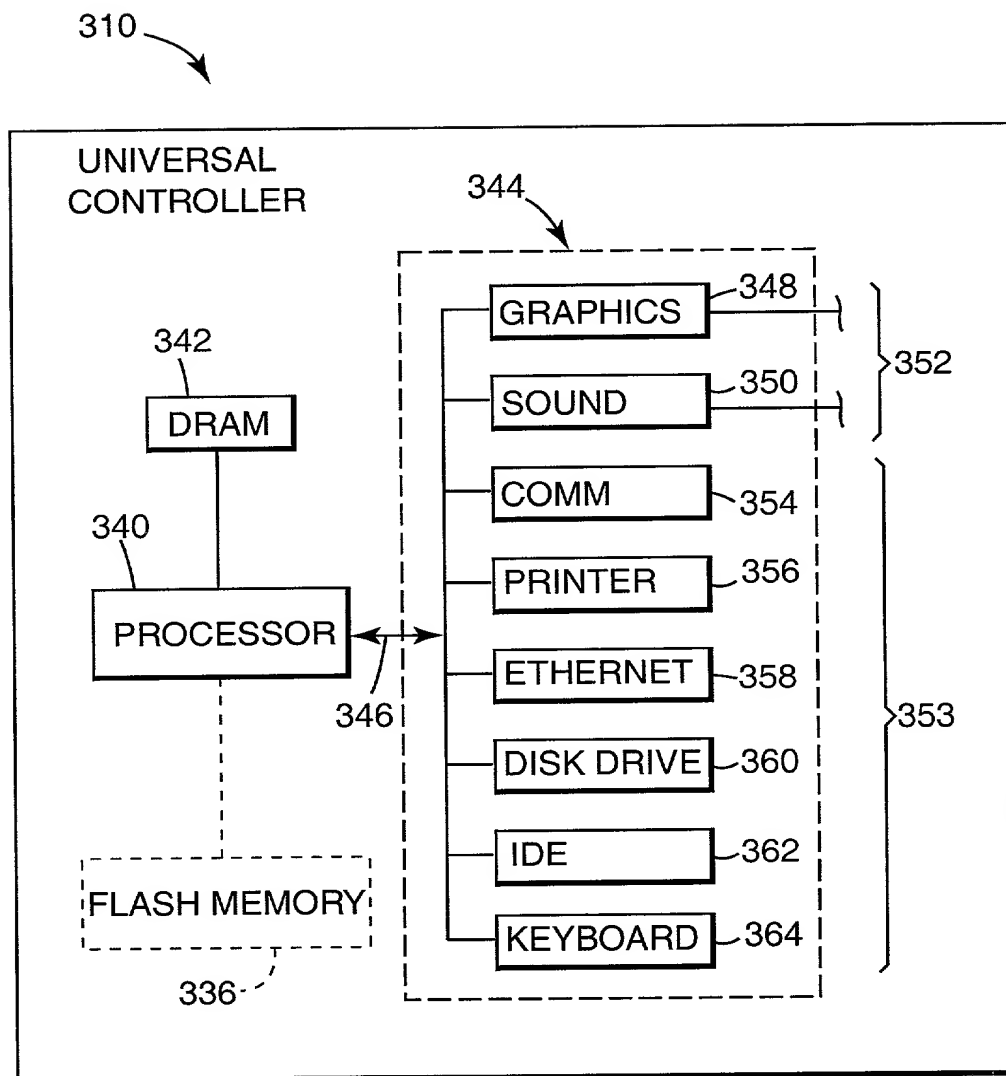


Fig. 7

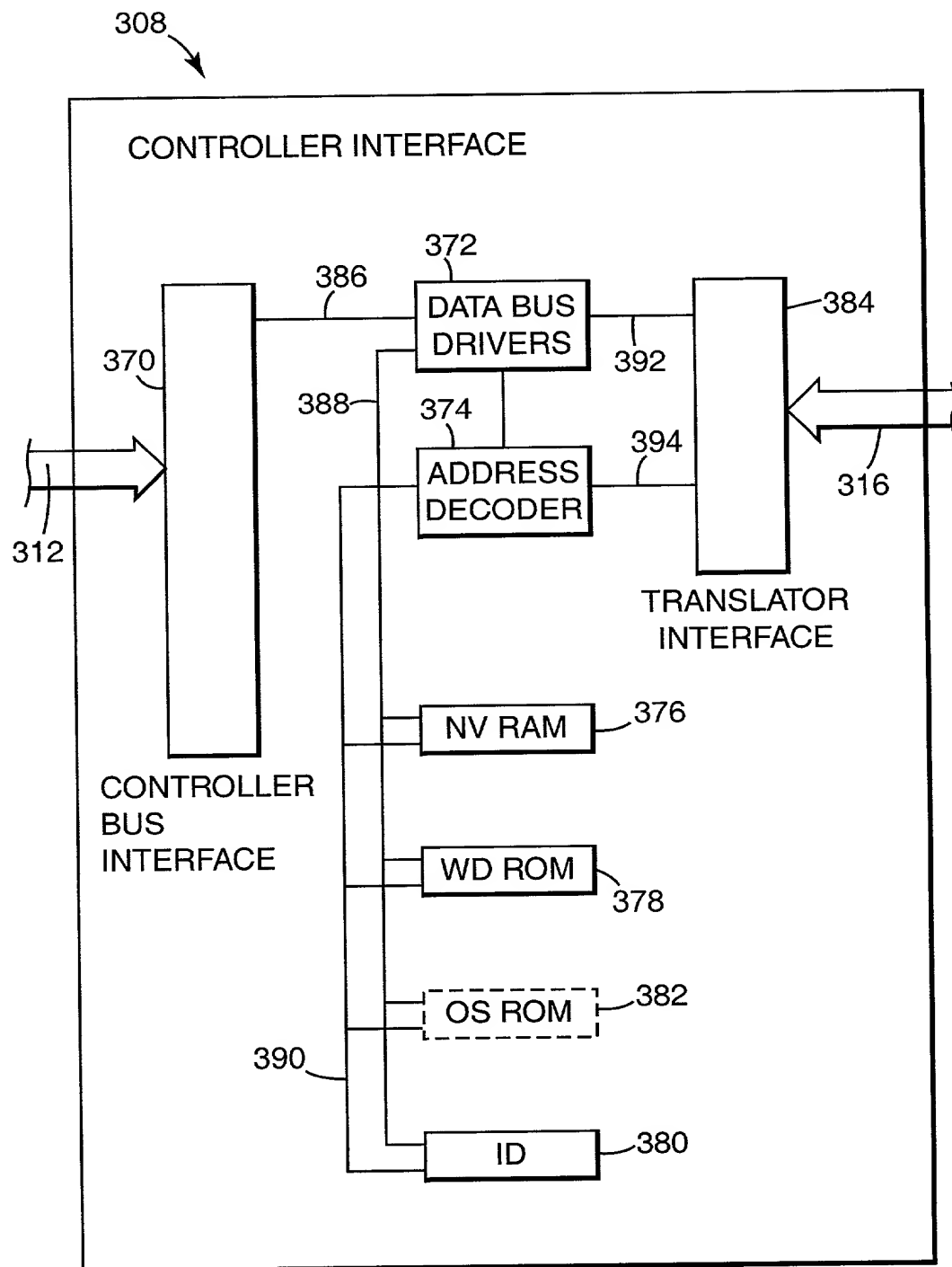


Fig. 8

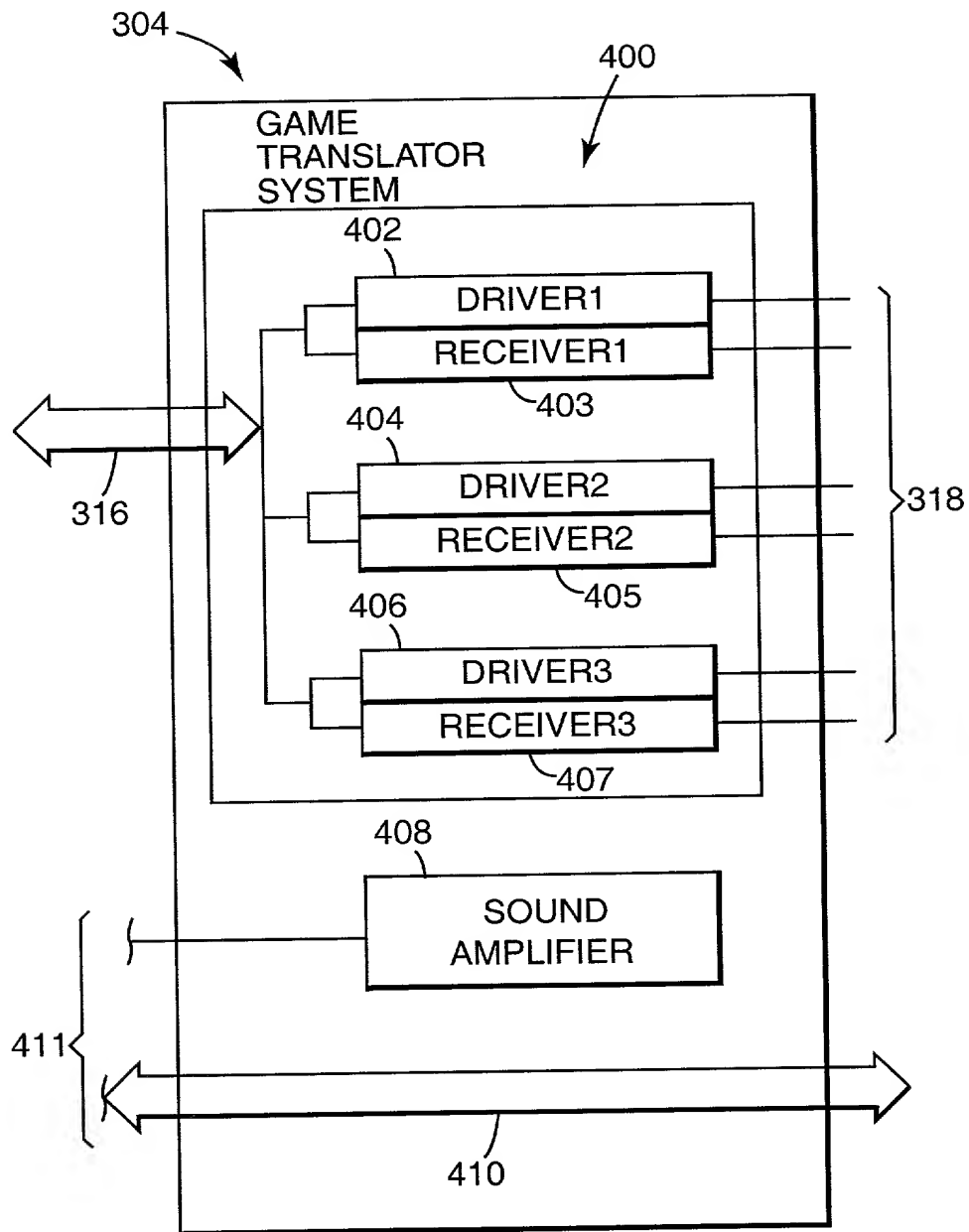


Fig. 9

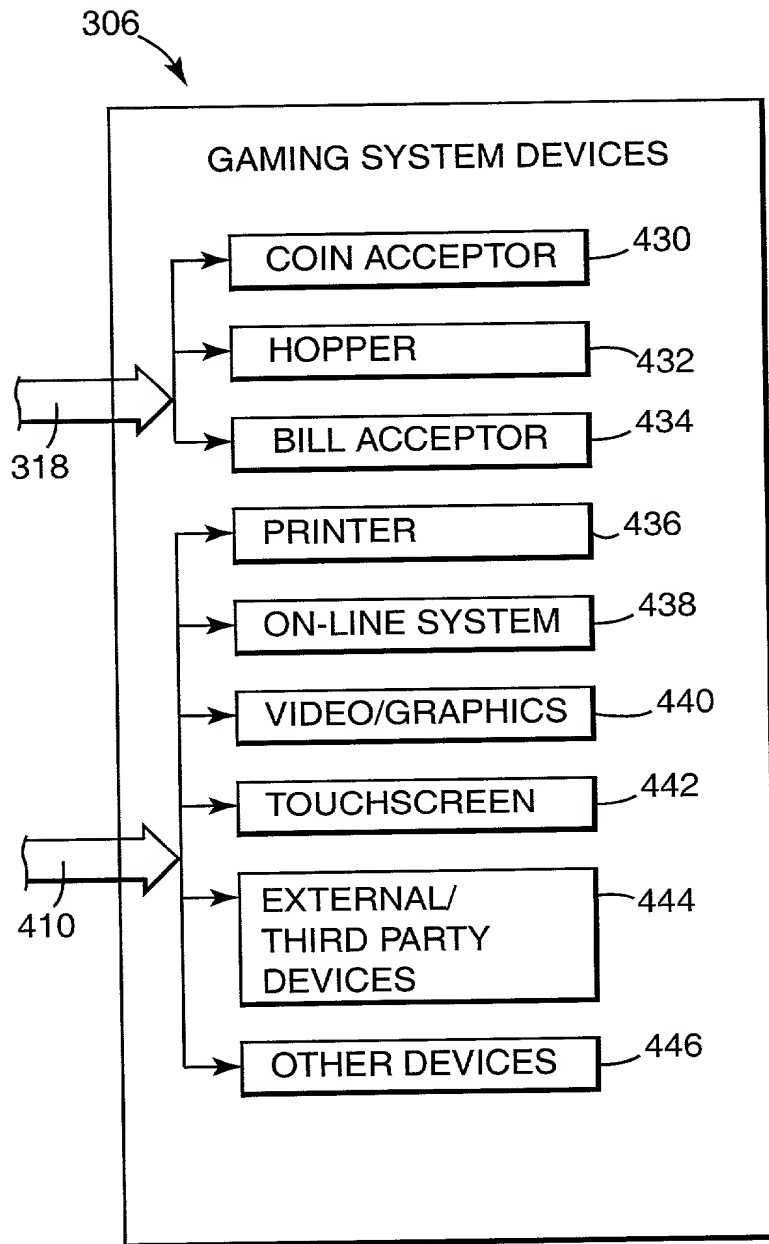


Fig. 10

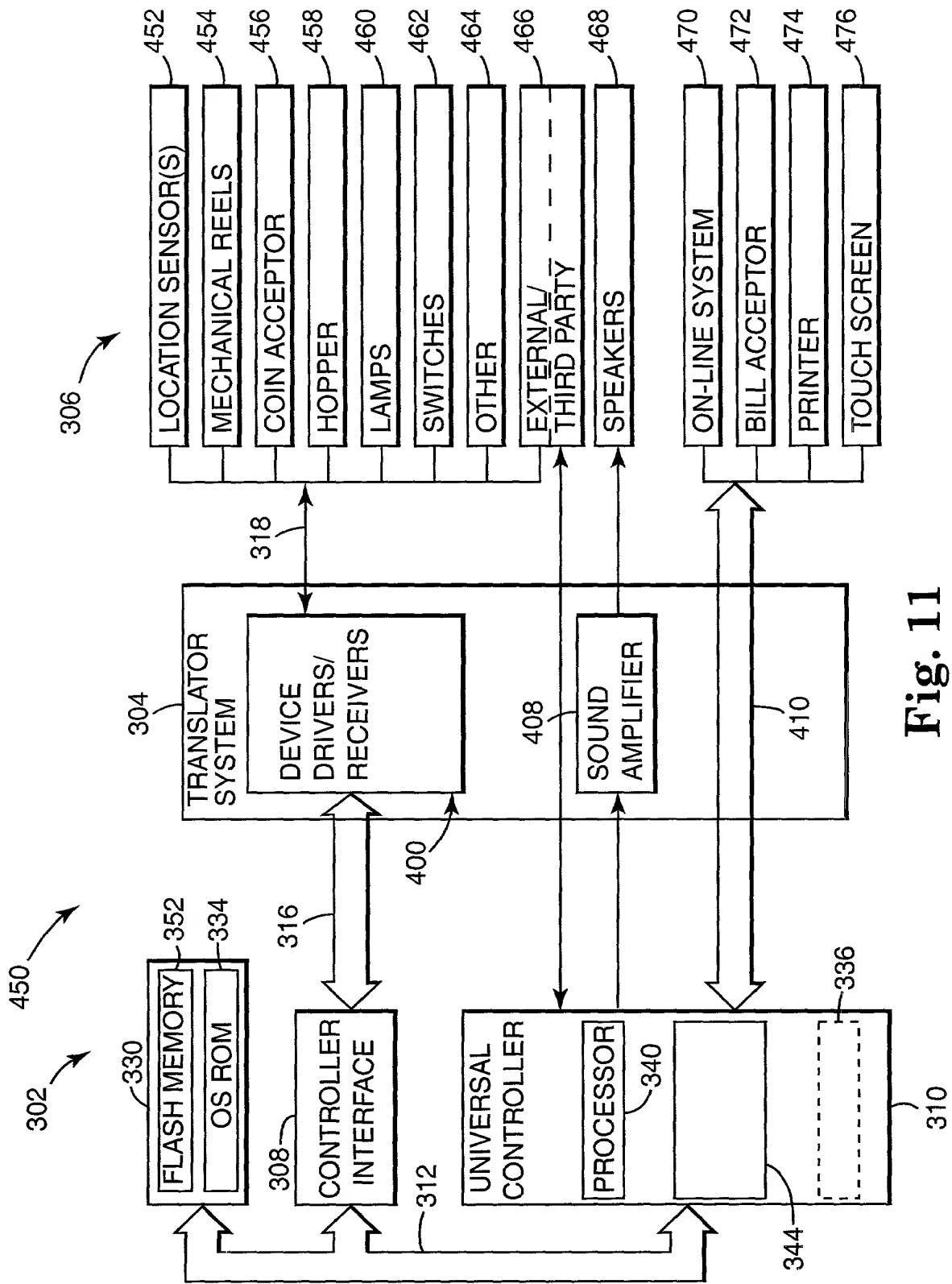


Fig. 11

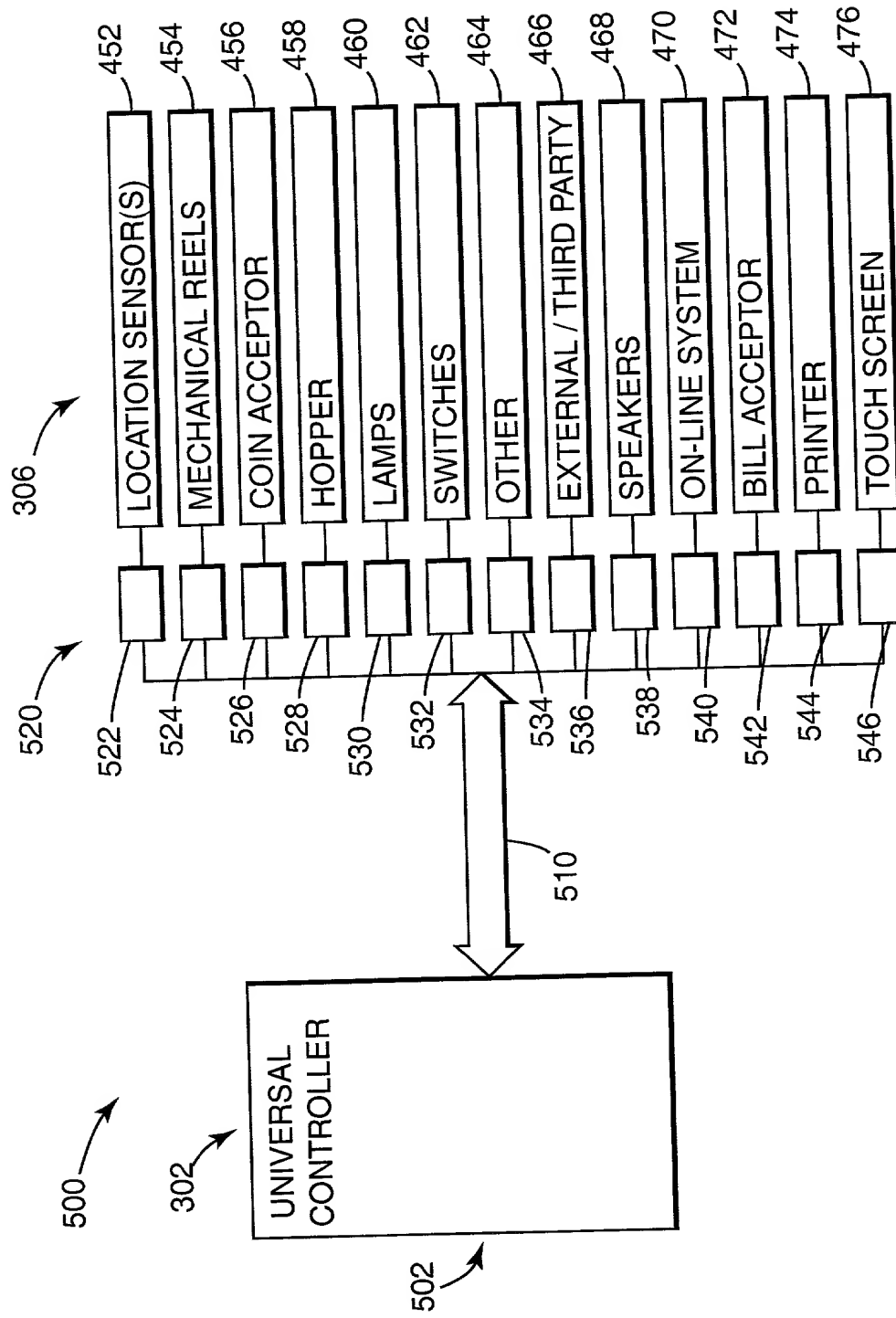


Fig. 12